

Electronic Acknowledgement Receipt

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Title of Invention:	Test Circuit in Clock Synchronous Semiconductor Memory Device
First Named Inventor/Applicant Name:	5511029
Customer Number:	20277
Filer:	Harold C. McGurk
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Power of Attorney	90012146_Power_Attorney_Re exam.pdf	1304116	no	1
			1d598b566c5e500bc0a482197b56079c8f29114		
Warnings:					
Information:					

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2	Reexam Certificate of Service	90012146_Certificate_Service_5-7-12.pdf	89631 b5f1a0988f540806e576913e54303a03c0e324f	no	2
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Total Files Size (in bytes):				1393747	
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